

Notice of References Cited

Application/Control No.

09/687,011

Applicant(s)/Patent Under
Reexamination
SAUL ET AL.

Examiner

Samuel Broda

Art Unit

2123

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,578,176 B1	06-2003	Wang et al.	716/2
	B	US-6,539,532 B1	03-2003	Levi et al.	716/16
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Govindarajan et al, "A Technique for Dynamic High-Level Exploration During Behavioral-Partitioning for Multi-Device Architectures," IEEE 13th International Conference on VLSI Design, pp. 212-219 (January 2000)
	V	Quan et al, "Preference-Driven Hierarchical Hardware/Software Partitioning," IEEE International Conference on Computer Design, pp. 652-657 (October 1999)
	W	Srinivasan et al, "Hardware Software Partitioning with Integrated Hardware Design Space Exploration," IEEE Proceedings of Design, Automation, and Test in Europe, pp. 28-35 (February 1998)
	X	Catania et al, "Applying Fuzzy Logic to Codesign Partitioning," IEEE Micro, pp. 62-70 (May/June 1997)

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.